

REMARKS

1. Request for Continued Examination

- 5 The applicant respectfully requests continued examination of the above-indicated application as per 37 CFR 1.114.

2. Amendments to the Claims

- 10 Claim 1 is currently amended to incorporate the claim lation of the original claim 2. The applicant believes that currently amended independent claim 1 better fully describes and uniquely points out the key features of the claimed invention with the incorporation of the limitation of claim 2. As a result, applicant has narrowed the claim scope of currently amended independent claim 1.
- 15 More Specifically, regarding currently amended independent claim 1 and original claim 2, original claim 2's claim limitation of the resistances of the first and second input impedances being close to each other. As is made clear by the disclosure of the present invention, the inventive amplifier circuit, such as that being illustrated in Fig.7,
- 20 is designed to have a first input impedance R_i and a second input impedance $R_i (1 + \alpha)$, wherein $|\alpha| \ll 1$; that is, the value of the first and second input impedance are very close. It is by such an arrangement that the inventive amplifier circuit can exhibit certain desirable electrical characteristics, such as particularly large time constant, high input impedance, or high voltage attenuation, and so on. Regarding this
- 25 amendment, no new matter is introduced. Consideration of currently amended independent claim 1 is respectfully requested.

- Applicant believes that the claimed limitation of the resistances of the first and second input impedances being close to each other is neither taught nor suggested by any
- 30 cited reference(s). Additionally, original claim 1 and original claim 2 are allowed as per Examiner's Notice of Allowance dated 03/06/2006, therefore, claim 1 amended by

the addition of original claim 2's limitation should also be allowable.

Claim 2 is amended to include the limitations of the first impedance being a switched capacitor circuit as shown in Fig. 6. This amendment is fully supported by Fig. 6.

5 Specifically, amended claim 2 now claims that the first input impedance is a switched capacitor circuit, the switched capacitor circuit comprises: a capacitor coupled between a first node and a second node; a first switch with one end coupled to the first node and another end used as an end of the switched capacitor circuit; a second switch coupled between the first node and a common node; a third switch with one end
10 coupled to the second node and another end used as another end of the switched capacitor circuit; and a fourth switch coupled between the second node and the common node

Claims 3 - 5 are currently amended to claim dependence on currently amended
15 independent claim 1 rather than currently amended claim 2. No new matter is introduced. Claims 3 - 21 are dependent upon the amended claim 1, and should be allowed if amended claim 1 is found allowable.

Claim 6 is amended to substitute the term "common node" for "ground end." The
20 inventor believes that in the present application, common node more uniquely and descriptively describes the disclosure. The substitution of "common node" for "ground end" is well supported by the specification as originally filed, since a "common node" can simply mean any node inside the inventive circuitry, of which a ground end or ground node as taught in the embodiments clearly serves as an example.

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Sincerely yours,

Winston Hsu

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